

# Professional Officer (All Levels) (Electrical/Electronic Engineering) Singapore Institute of Technology

Direct Link: https://www.AcademicKeys.com/r?job=254407

Downloaded On: Apr. 2, 2025 7:41pm Posted Mar. 14, 2025, set to expire Jul. 14, 2025

Job Title Professional Officer (All Levels) (Electrical/Electronic Engineering)

**Department** Professional Officers

**Institution** Singapore Institute of Technology

Singapore, , Singapore

Date Posted Mar. 14, 2025

Application Deadline Open until filled

Position Start Date Available immediately

Job Categories Professional Staff

Academic Field(s) Electrical and/or Electronics

Job Website https://careers.singaporetech.edu.sg/cw/en/job/498862/professional-

officer-all-levels-electricalelectronic-engineering

**Apply By Email** 

**Job Description** 

### Professional Officer (All Levels) (Electrical/Electronic Engineering)

**Job no:** 498862

**Department:** Professional Officers

Contract type: Contract

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The Professional Officers Division (POD) houses a centralized pool of academic staff under the



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Professional Officers scheme. Professional Officers come with specialised and deep technical skills acquired through extensive industry experience. They form the talent pool which brings a much-needed industry perspective to student learning. They facilitate applied learning and applied research in SIT, complementing the academic expertise of the faculty to bring industry practices and applications into the curriculum.

Professional Officers leverage on their industry experiences to create authentic learning environments, where discovery and innovation take place. They act as coaches and mentors to students during practical learning activities such as laboratory sessions, Capstone Projects and the Integrated Work Study Programme (IWSP). Professional Officers could also lead or work with faculty on industry innovation projects to provide solutions to the industry.

In addition to their role in applied learning and applied research, Professional Officers manage the centralised laboratory facilities and resources in SIT. With Technical Officers, laboratory safety professionals, and administrators in POD, they jointly develop central policies and processes for the safe and seamless operation of laboratories in SIT.

### **Key Responsibilities**

- Design and teach labs & practice modules.
- Mentor students in Capstone Projects and the Integrated Work Study Programme (IWSP).
- Lead or co-lead innovation projects with industry.
- Manage labs and equipment to support academic programmes and applied research.
- Ensure safety in labs.

#### Job Requirements

- 1. A good degree in Electrical/Electronics Engineering or related fields. Possessing a Masters, PhD and/or relevant professional certifications would be advantageous.
- 2. Deep technical specialist with 10 or more years of working experience in Electrical/Electronics Engineering environment.
- 3. Have deep, hands-on competence in the following areas:
  - High voltage switching
  - Engineering design, modelling & analysis of conventional power, wind & solar farms
  - Engineering design of transmission & distribution substations, electrical distribution systems of industries and commercial buildings



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- Power system protection application and relay coordination
- Digital/Analogue/Power electronics
- IoT/Micro-controller application
- 4. It will be advantageous with working knowledge of/as:
  - Knowledge in programming/using Python, MATLAB, LTspice, IoT platform.
  - Authorised high voltage switching Engineer.
- 5. Extensive hands-on experience and knowledge of industry practices and engineering principles to bring industry perspective to SIT.
- 6. Keen interest in innovation projects, with demonstrated ability in developing solutions to technical problems.
- 7. Strong supervisory skills and enjoy working closely with students in an educational environment.
- 8. Demonstrate proficiency to keep abreast of development in the field and pursue professional certification programs. Possess industrial certifications in relevant areas will be an added advantage.

#### Apply now

Advertised: 14 Mar 2025 Singapore Standard Time

#### **Contact Information**

Please reference Academickeys in your cover letter when applying for or inquiring about this job announcement.

Contact

Singapore