

Professional Officer (All Levels) (Electronics Engineering)
Singapore Institute of Technology

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Posted Mar. 14, 2025, set to expire Jul. 14, 2025

Job Title Professional Officer (All Levels) (Electronics Engineering)
Department
Institution Singapore Institute of Technology
Singapore, , Singapore

Date Posted Mar. 14, 2025

Application Deadline Open until filled

Position Start Date Available immediately

Job Categories Professional Staff

Academic Field(s) Electrical and/or Electronics

Job Website <https://careers.singaporetech.edu.sg/cw/en/job/498863/professional-officer-all-levels-electronics-engineering>

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Job Description

Professional Officer (All Levels) (Electronics Engineering)

Job no: 498863

Department: Professional Officers

Contract type: Contract

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The Professional Officers Division (POD) houses a centralized pool of academic staff under the Professional Officers scheme. Professional Officers come with specialised and deep technical skills

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acquired through extensive industry experience. They form the talent pool which brings a much-needed industry perspective to student learning. They facilitate applied learning and applied research in SIT, complementing the academic expertise of the faculty to bring industry practices and applications into the curriculum.

Professional Officers leverage on their industry experiences to create authentic learning environments, where discovery and innovation take place. They act as coaches and mentors to students during practical learning activities such as laboratory sessions, Capstone Projects and the Integrated Work Study Programme (IWSP). Professional Officers could also lead or work with faculty on industry innovation projects to provide solutions to the industry.

In addition to their role in applied learning and applied research, Professional Officers manage the centralised laboratory facilities and resources in SIT. With Technical Officers, laboratory safety professionals, and administrators in POD, they jointly develop central policies and processes for the safe and seamless operation of laboratories in SIT.

Key Responsibilities

- Design and teach labs & practice modules.
- Mentor students in Capstone Projects and the Integrated Work Study Programme (IWSP).
- Lead or co-lead innovation projects with industry.
- Manage labs and equipment to support academic programmes and applied research.
- Ensure safety in labs.

Job Requirements

1. A good degree with a major in Electronics Engineering or a related field. Masters, PhD and/or relevant professional certifications would be advantageous.
2. Deep technical specialist with 10 or more years of working experience in development / R&D or related areas in electronics (Digital / Analog / Consumer / Embedded Systems) would be preferred.
3. Experience in following areas is highly desirable.
 - Hands-on experience with both analogues and digital electronics
 - Hands-on experience in developing, implementing applications, principles and programming in areas of embedded systems, IoT/IIoT, control systems, etc.

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- Design of circuit schematics for layouts and development with both active and passive components, incl. knowledge of PCB routing, fabrication and prevalent IPC standards.
- Hands-on competency in testing and designing electronic circuits and integrating circuits and their systems.
- Advantageous to have prior knowledge in areas such as:
 - Microcontroller programming (C/C++, Python, LabView) and firmware development.
 - Working experience with IoT, data analytics, artificial intelligence, machine learning, etc.
 - Working experience with various development boards systems like Arduino, Raspberry Pi, Intel development boards, etc.
- Extensive hands-on experience and knowledge of industry practices and engineering principles to bring industry perspective to SIT.
- Keen interest in innovation projects, with demonstrated ability in developing solutions to technical problems.
- Strong supervisory skills and enjoy working closely with students in an educational environment.
- Demonstrate proficiency to keep abreast of development in the field and pursue professional certification programs. Possess industrial certifications in relevant areas will be an added advantage.

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Advertised: 14 Mar 2025 Singapore Standard Time

Applications close: 12 Sep 2025 Singapore Standard Time

Contact Information

Please reference Academickeys in your cover letter when applying for or inquiring about this job announcement.

Contact

Singapore