

INESC TEC | Research Grant (AE2025-0128) INESC TEC

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Posted Mar. 17, 2025, set to expire Jul. 14, 2025

Job Title	INESC TEC Research Grant (AE2025-0128)
Department	CTM
Institution	INESC TEC PORTO, , Portugal
Date Posted	Mar. 17, 2025
Application Deadline	Mar. 27, 2025
Position Start Date	Mar. 13, 2025
Job Categories	Graduate Student
Academic Field(s)	Electrical and/or Electronics Engineering - Other Computer Science
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Job Description	

Research Opportunities

Engenharia

Work description

- Familiarization with the PolarFile SoC platform, which will be used for implementation.
- Selection and integration of an existing state-of-the-art CGRA for integration with the RISC-V processor on the target PolarFire board.

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- Implementation of the hardware needed to interpret a set of custom instructions for controlling a CGRA (Coarse Grained Reconfigurable Array) type hardware accelerator.
- Collaboration in the writing of a scientific article to disseminate the results.

Academic Qualifications

- Graduation or enrollment in a master's degree in electrical engineering, computer science, or a related field.

Minimum profile required

- Experience in HDL and C++.
- Fluency in English (written and spoken).

Preference factors

- Experience in RISC-V.
- Experience in FPGA or heterogeneous systems.
- Fluent in Portuguese and English (written and spoken).

Application Period

Since 13 Mar 2025 to 27 Mar 2025

Centre

Telecommunications and Multimedia

Scientific Advisor

[Nuno Miguel Paulino](#)

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- **Other Benefits and Perks**
- Flexible working time, health insurance, discounts in hotels, transportation, etc.
- **Informal Events**
- Annual informal events, such as the multicultural party.

For more information: [Click Here](#)

Contact Information

Please reference Academickeys in your cover letter when applying for or inquiring about this job announcement.

Contact

Portugal

Contact E-mail rh@inesctec.pt