

Research Associate/Research Fellow (RFIC design and Academic)
Nanyang Technological University

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Posted May 6, 2025, set to expire Sep. 4, 2025

Job Title	Research Associate/Research Fellow (RFIC design and Academic)
Department	School of Electrical and Electronic Engineering
Institution	Nanyang Technological University Singapore, , Singapore
Date Posted	May 6, 2025
Application Deadline	Open untill filled
Position Start Date	Available Immediately
Job Categories	Research Scientist/Associate
Academic Field(s)	Electrical and/or Electronics
Job Website	https://ntu.wd3.myworkdayjobs.com/Careers/job/NTU-Main-Campus-Singapore/Research-Associate-Research-Fellow--RFIC-design-and-Academic-_R00020619
Apply Online Here	https://ntu.wd3.myworkdayjobs.com/Careers/job/NTU-Main-Campus-Singapore/Research-Associate-Research-Fellow--RFIC-design-and-Academic-_R00020619

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Job Description

School of Electrical and Electronic Engineering is one of the founding Schools of the Nanyang Technological University. Built on a culture of excellence, the School is renowned for its high academic standards and research. With over 3,000 undergraduates students and 2,000 graduate students it is

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one of the largest EEE schools in the world and ranks 4th in the field of Electrical & Electronic Engineering in the 2025 QS World University Rankings by Subjects.

Today, the School has become one of the world's largest engineering schools that nurtures competent engineers and researchers. Each year, the School graduates over a thousand students who are ready to take on great ambitions and challenges.

For more details, please view: <https://www.ntu.edu.sg/eee>.

The Centre for Integrated Circuits and Systems (CICS) at the School of EEE is a globally recognized IC research hub with advanced EDA tools and measurement facilities. It provides access to industry-standard platforms (e.g., Cadence, Synopsys, Ansys) supporting CMOS to III-V technologies, including radiation effect simulations. CICS features world-class RF/mm-wave testing tools, such as a 12" probe station for on-wafer measurements up to 325GHz, and supports research in analog/mixed-signal, RF/mm-wave, biomedical, power management, and emerging devices.

We are hiring a Research Associate/Research Fellow in RFIC design to develop advanced radio frequency integrated circuits for next-generation wireless communication systems. The Research Associate/Research Fellow is also expected to support teaching activities as required by the school.

Key Responsibilities:

- Conduct research and development of advanced RF integrated circuits (RFICs) for next-generation wireless communication systems, including 5G/6G and IoT applications.
- Design, simulate, and layout of RF/mm-wave circuits such as LNAs, mixers, VCOs, PAs, and transceivers using industry-standard EDA tools (e.g., Cadence, Keysight ADS).
- Perform post-layout verification, chip tape-out, and collaborate with foundries for fabrication.
- Carry out measurement, characterization, and debugging of fabricated ICs using test equipment.
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Prepare technical reports, publish research findings in high-impact journals/conferences, and contribute to project proposals and grant applications.

- Prepare report and conduct presentation at seminars.
- Assist the school in various teaching activities, including instructing or supervision of labs and tutorials.

Job Requirements:

- MSc/MEng (Research Associate) or PhD (Research Fellow) in Electrical and Electronic Engineering or other related fields.
- Expertise in RFIC design, including circuit modeling, simulation, and layout design techniques.
- Proficiency with using relevant tools such as ADS, HFSS, Cadence and Matlab etc.
- Familiarity with RF/mm-wave transceiver architectures and components (low-noise amplifiers (LNAs), power amplifiers (PAs), mixers, oscillators etc)
- Experience in designing analog and mixed-signal circuits for RF applications.
- Knowledgeable in python programming, AI/ML techniques for optimizing RFIC design and system performance would be an added advantage.
- Able to work independently and possesses strong research skills.
- Excellent verbal communication and technical writing skills

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We regret to inform that only shortlisted candidates will be notified.

Contact Information

Please reference Academickeys in your cover letter when
applying for or inquiring about this job announcement.

Contact

Singapore