# R & D Engineer 2 (4757) Job 80686 - Electrical Engineering and Computer Science (EECS) University of California, Berkeley



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| **Job Title** | R & D Engineer 2 (4757) Job 80686 - Electrical Engineering and Computer Science (EECS) |
| **Department** | Electrical Engineering and Computer Science |
| **Institution** | University of California, Berkeley<br>Berkeley, California |
| **Date Posted** | Aug. 26, 2025 |
| **Application Deadline** | Open until filled |
| **Position Start Date** | Available immediately |
| **Job Categories** | Research Scientist/Associate<br>Professional Staff |
| **Academic Field(s)** | Electrical and/or Electronics |
| **Apply Online Here** | https://apptrkr.com/6507524 |
| **Apply By Email** | |

**Job Description**

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### About Berkeley

At the University of California, Berkeley, we are dedicated to fostering a community where everyone feels welcome and can thrive. Our culture of openness, freedom and belonging make it a special place for students, faculty and staff.

As a world-leading institution, Berkeley is known for its academic and research excellence, public

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mission, diverse student body, and commitment to equity and social justice. Since our founding in 1868, we have driven innovation, creating global intellectual, economic and social value.

We are looking for applicants who reflect California's diversity and want to be part of an inclusive, equity-focused community that views education as a matter of social justice. Please consider whether your values align with our Guiding Values and Principles, Principles of Community, and Strategic Plan.

At UC Berkeley, we believe that learning is a fundamental part of working, and provide space for supportive colleague communities via numerous employee resource groups (staff organizations). Our goal is for everyone on the Berkeley campus to feel supported and equipped to realize their full potential. We actively support this by providing all of our full-time staff employees with at least 80 hours (10 days) of paid time per year to engage in professional development activities. Find out more about how you can grow your career at UC Berkeley.

**Departmental Overview**

The Department of Electrical Engineering and Computer Sciences (EECS) at UC Berkeley offers one of the strongest research and instructional programs in this field anywhere in the world. Our key strength is our array of cross-disciplinary, team-driven projects. The integration of EE and CS forms the core, with strong interactions that extend into biological sciences, mechanical and civil engineering, physical sciences, chemistry, mathematics, and operations research.

The Reconfigurable Quantum Materials Laboratory (Cao Lab) is seeking an R&D Engineer to lead and oversee all nanofabrication projects within the research group. The primary purpose of this position is to develop new fabrication processes for advanced MEMS devices, for quantum and energy-related research projects.

**Application Review Date**

The First Review Date for this job is: 9/4/25 - Open until filled

**Responsibilities**

45% - Under supervision, design, fabricate, and use electronic test equipment for characterizing MEMS devices and other nanoelectronic devices, such as transistors, switches, and filters. This includes a significant amount of computer-aided design work, as well as laboratory work such as soldering, measuring, and debugging. Simulates and optimizes MEMS devices using COMSOL.

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20% - Under supervision, design and fabricate MEMS and other nanoelectronic devices such as transistors, switches, and filters in a nanofabrication facility (e.g., Marvell Nanolab). This includes working in a cleanroom environment, using shared facilities such as lithography, etching, thin-film growth tools, etc., as well as wet-bench work that involves using various chemicals, for the processing of silicon and III-V semiconductor wafers and materials.

15% - Gathers and analyzes data; prepares formal engineering reports, drawings, and status reports. Analyzes the performance of the MEMS device via electrical measurements.

10% - Work subject to review by a more senior-level Engineer.

10% - Assists with the implementation of business processes.

**Required Qualifications**

- Working knowledge of engineering principles and methods to independently perform professional engineering design work of limited scope and complexity.
- Working knowledge necessary to begin independent application of principles, practices, and procedures in the completion of assignments.
- Organizational abilities and decision-making to prioritize work assignments.
- Effective written and verbal communication skills.
- Ability to work collaboratively, to assist in identifying any challenges or barriers.
- Bachelor's degree in a related area and/or equivalent experience/training.

**Salary & Benefits**

For information on the comprehensive benefits package offered by the University, please visit the University of California's Compensation & Benefits website.

Under California law, the University of California, Berkeley is required to provide a reasonable estimate of the compensation range for this role and should not offer a salary outside of the range posted in this job announcement. This range takes into account the wide range of factors that are considered in making compensation decisions, including but not limited to experience, skills, knowledge, abilities, education, licensure and certifications, analysis of internal equity, and other business and organizational needs. It is not typical for an individual to be offered a salary at or near the top of the range for a position. Salary offers are determined based on final candidate qualifications and

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experience.

The budgeted salary or hourly range that the University reasonably expects to pay for this position is $80,000 - 95,000.

**Conviction History Background**

This is a designated position requiring fingerprinting and a background check due to the nature of the job responsibilities. Berkeley does hire people with conviction histories and reviews information received in the context of the job responsibilities. The University reserves the right to make employment contingent upon successful completion of the background check.

**Misconduct Disclosure**

As a condition of employment, the final candidate who accepts a conditional offer of employment will be required to disclose if they have been subject to any final administrative or judicial decisions within the last seven years determining that they committed any misconduct; received notice of any allegations or are currently the subject of any administrative or disciplinary proceedings involving misconduct; have left a position after receiving notice of allegations or while under investigation in an administrative or disciplinary proceeding involving misconduct; or have filed an appeal of a finding of misconduct with a previous employer.

"Misconduct" means any violation of the policies or laws governing conduct at the applicant's previous place of employment, including, but not limited to, violations of policies or laws prohibiting sexual harassment, sexual assault, or other forms of harassment, discrimination, dishonesty, or unethical conduct, as defined by the employer. For reference, below are UC's policies addressing some forms of misconduct:

UC Sexual Violence and Sexual Harassment Policy
UC Anti-Discrimination Policy
Abusive Conduct in the Workplace

**Equal Employment Opportunity**

The University of California is an Equal Opportunity Employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex, sexual orientation, gender identity, national origin, disability, age, protected veteran status, or other protected status under state

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or federal law.

**To apply, visit**
**https://careerspub.universityofcalifornia.edu/psc/ucb/EMPLOYEE/HRMS/c/HRS_HRAM_FL.HRS_CG_S**

## Contact Information

Please reference Academickeys in your cover letter when applying for or inquiring about this job announcement.

**Contact**

N/A
University of California, Berkeley
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