

INESC TEC | INESC TEC

Direct Link: <https://www.AcademicKeys.com/r?job=230386>

Downloaded On: May. 12, 2024 10:08am

Posted Feb. 8, 2024, set to expire Jun. 9, 2024

Job Title	INESC TEC
Department	CTM
Institution	INESC TEC PORTO, , Portugal
Date Posted	Feb. 8, 2024
Application Deadline	Feb. 28, 2024
Position Start Date	Feb. 9, 2024
Job Categories	Graduate Student
Academic Field(s)	Engineering - Other
Apply Online Here	https://www.inesctec.pt/en/opportunities/engineering-AE2024-0055
Apply By Email	
Job Description	

Research Opportunity

Engineering

Work description

1. Study of circuit models representing the behavior of memristors.
2. Study and propose circuit architectures for controlling memristors, taking into account the need for

INESC TEC | INESC TEC

Direct Link: <https://www.AcademicKeys.com/r?job=230386>

Downloaded On: May. 12, 2024 10:08am

Posted Feb. 8, 2024, set to expire Jun. 9, 2024

scalability.

3. Collaborate in the development of antenna prototypes that integrate memristors.
4. Collaborate in the submission of scientific articles.
5. Write the scholarship activity report.

Academic Qualifications

MSc in Electrical Engineering, or similar.

Minimum profile required

- Experience with circuit simulation tools (e.g. LTSpice);
- Experience with the experimental characterisation of devices on probe station.

Preference factors

Experience with the experimental characterisation of memristors.

Application Period

Since 09 Feb 2024 to 28 Feb 2024

Cluster / Centre

Networked Intelligent Systems / Telecommunications and Multimedia

Scientific Advisor

INESC TEC |
INESC TEC

Direct Link: <https://www.AcademicKeys.com/r?job=230386>

Downloaded On: May. 12, 2024 10:08am

Posted Feb. 8, 2024, set to expire Jun. 9, 2024

Vítor Grade Tavares

Contact Information

Please reference Academickeys in your cover letter when applying for or inquiring about this job announcement.

Contact

Portugal

Contact E-mail rh@inesctec.pt